Techno India NJR Institute of Technology

Academic Administration of Techno NJR Institute

Lab Deployment

Name of Faculty: Mr Yogendra Singh Solanki	Subject Code: 5EX4-23
Subject Name: Microprocessor Lab	SEM: V
Department: Department of Electrical and Electronics Engineering	
Total No. of Labs Planned: 10	

COURSE OUTCOMES

At the end of this course students will be able to:

CO1: To perform the microprograms like addition, subtraction etc.
CO2: To perform the Transfer a block of data from memory location XX00 to Another memory location XX00 in forward & reverse order.
CO3: To perform the operation on peripheral devices.

Labs No.	Name of Experiment
1	Study the hardware, functions, memory structure and operation of 8085-Microprocessor kit.
2	Program to perform integer division: (1) 8-bit by 8-bit (2) 16-bit by 8-bit.
3	• Transfer of a block of data in memory to another place in memory. • Transfer of black to another location in reverse order.
4	• Finding party of a 32-bit number. • Program to perform following conversion (1) BCD to ASCII (2) BCD to hexadecimal

5	• Program to multiply two 8–bit numbers • Program to generate and sum 15 Fibonacci numbers.
6	• Program for rolling display of message "India", "HELLO". • To insert a number at correct place in a sorted array.
7	• Reversing bits of an 8-bit number. • Fabrication of 8-bit LED interfaces for 8085 kit through 8155 and 8255.
8	Data transfer on output port 8155 & 8255 & implementation of disco light, running light, and sequential lights on the above mentioned hardware.
9	Parallel data transfer between two DYNA-85 kit using 8253 ports.
10	Generation of different waveform on 8253/8254 programmable timer.

TEXT/REFERENCE BOOKS
1. Microprocessors Architecture, Programming &Application, Ramesh S. Gaonkar, (2000).
2. A Textbook of Microprocessors and Microcontrollers, R.S. Kaler I.K International Publishing House Pvt. Ltd.
3. Introduction to Microprocessors, A.P. Mathur, Mc Graw Hill.

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पंकज पोरवाल

Dr. Pankaj Kumar Porwal
(Principal)