

Syllabus of  
UNDERGRADUATE DEGREE COURSE

Electrical Engineering



Rajasthan Technical University, Kota  
Effective from session: 2021 – 2022



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

2<sup>nd</sup> Year - IV Semester: B.Tech. (Electrical Engineering)

4EE2-01: Biology

Credit: 2  
2L+0T+0P

Max. Marks: 100(IA:30, ETE:70)  
End Term Exam: 3 Hours

SN	CONTENTS	Hours
1	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2	<b>Introduction:</b> Purpose: To convey that Biology is as important a scientific discipline as Mathematics, Physics and Chemistry. Bring out the fundamental differences between science and engineering by drawing a comparison between eye and camera, Bird flying and aircraft. Mention the most exciting aspect of biology as an independent scientific discipline. Why we need to study biology? Discuss how biological observations of 18th Century that lead to major discoveries. Examples from Brownian motion and the origin of thermodynamics by referring to the original observation of Robert Brown and Julius Mayor. These examples will highlight the fundamental importance of observations in any scientific inquiry.	1
3	<b>Classification:</b> Purpose: To convey that classification <i>per se</i> is not what biology is all about. The underlying criterion, such as morphological, biochemical or ecological be highlighted. Hierarchy of life forms at phenomenological level. A common thread weaves this hierarchy Classification. Discuss classification based on (a) cellularity- Unicellular or multicellular (b) ultrastructure prokaryotes or eucaryotes. (c) energy and Carbon utilization -Autotrophs, heterotrophs, lithotropes (d) Ammonia excretion- aminotelic, uricotelic, ureotelic (e) Habitata- aquatic or terrestrial (e) Molecular taxonomy- three major kingdoms of life. A given organism can come under different category based on classification. Model organisms for the study of biology come from different groups. E.coli, S.cerevisiae, D. Melanogaster, C. elegance, A. Thaliana, M. musculus	3
4	<b>Genetics:</b> Purpose: To convey that "Genetics is to biology what Newton's laws are to Physical Sciences". Mendel's laws, Concept of segregation and independent assortment. Concept of allele. Gene mapping, Gene interaction, Epistasis. Meiosis and Mitosis be taught as a part of genetics. Emphasis to be give not to the mechanics of cell division nor the phases but how genetic material passes from parent to offspring. Concepts of recessiveness and dominance. Concept of mapping of phenotype to genes. Discuss about the single gene disorders in humans. Discuss the concept of complementation using human genetics.	3
5	<b>Biomolecules:</b> Purpose: To convey that all forms of life has the same building blocks and yet the manifestations are as diverse as one can imagine. Molecules of life. In this context discuss monomeric units and polymeric structures. Discuss about sugars, starch and cellulose. Amino acids and proteins. Nucleotides and DNA/RNA. Two carbon units and lipids.	3

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6	<b>Enzymes:</b> Purpose: To convey that without catalysis life would not have existed on earth. Enzymology: How to monitor enzyme catalysed reactions. How does an enzyme catalyse reactions? Enzyme classification. Mechanism of enzyme action. Discuss at least two examples. Enzyme kinetics and kinetic	3
7	<b>Information Transfer:</b> Purpose: The molecular basis of coding and decoding genetic information is universal. Molecular basis of information transfer. DNA as a genetic material. Hierarchy of DNA structure- from single stranded to double helix to nucleosomes. Concept of genetic code. Universality and degeneracy of genetic code. Define gene in terms of complementation and recombination.	3
8	<b>Macromolecular analysis:</b> Purpose: To analyse biological processes at the reductionistic level. Proteins- structure and function. Hierarch in protein structure. Primary secondary, tertiary and quaternary structure. Proteins as enzymes, transporters, receptors and structural elements.	4
9	<b>Metabolism:</b> Purpose: The fundamental principles of energy transactions are the same in physical and biological world. Thermodynamics as applied to biological systems. Exothermic and endothermic versus endergonic and exergonic reactions. Concept of $K_{eq}$ and its relation to standard free energy. Spontaneity. ATP as an energy currency. This should include the breakdown of glucose to $CO_2 + H_2O$ (Glycolysis and Krebs cycle) and synthesis of glucose from $CO_2$ and $H_2O$ (Photosynthesis). Energy yielding and energy consuming reactions. Concept of Energy charge.	4
10	<b>Microbiology:</b> Concept of single celled organisms. Concept of species and strains. Identification and classification of microorganisms. Microscopy. Ecological aspects of single celled organisms. Sterilization and media compositions. Growth kinetics.	3
<b>Total</b>		<b>28</b>



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2<sup>nd</sup> Year - IV Semester: B.Tech. (Electrical Engineering)

**4EE1-03/3EE1-03: Managerial Economics and Financial Accounting**

**Credit: 2**  
**2L+0T+0P**

**Max. Marks: 100(IA:30, ETE:70)**  
**End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2	<b>Basic economic concepts</b> Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	3
3	<b>Demand and Supply analysis</b> Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
4	<b>Production and Cost analysis</b> Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
5	<b>Market structure and pricing theory</b> Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
6	<b>Financial statement analysis</b> Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
<b>TOTAL</b>		<b>26</b>

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## SYLLABUS

2<sup>nd</sup> Year - IV Semester: B.Tech. (Electrical Engineering)

### 4EE1-02/3EE1-02: Technical Communication

**Credit: 2**  
**2L+0T+0P**

**Max. Marks: 100(IA:30, ETE:70)**

**End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2	<b>Introduction to Technical Communication-</b> Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	3
3	<b>Comprehension of Technical Materials/Texts and Information Design &amp; development-</b> Reading of technical texts, Reading and comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note-making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
4	<b>Technical Writing, Grammar and Editing-</b> Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
5	<b>Advanced Technical Writing-</b> Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
	<b>TOTAL</b>	<b>26</b>

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## SYLLABUS

2<sup>nd</sup> Year - IV Semester: B.Tech. (Electrical Engineering)

4EE3-04: Electronic Measurement and Instrumentation

Credit: 2  
2L+0T+0P

Max. Marks: 100(IA:30, ETE:70)  
End Term Exam: 2 Hours

SN	CONTENTS	Hours
1	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2	<b>Measuring Instruments:</b> Moving coil, moving iron, electrodynamic and induction instruments-construction, operation, torque equation and errors. Applications of instruments for measurement of current, voltage, single-phase power and single-phase energy. Errors in wattmeter and energy meter and their compensation and adjustment. Testing and calibration of single-phase energy meter by phantom loading.	4
3	<b>Polyphase Metering:</b> Blondel's Theorem for n-phase, p-wire system. Measurement of power and reactive kVA in 3-phase balanced and unbalanced systems: One-wattmeter, two-wattmeter and three-wattmeter methods. 3-phase induction type energy meter. Instrument Transformers: Construction and operation of current and potential transformers. Ratio and phase angle errors and their minimization. Effect of variation of power factor, secondary burden and frequency on errors. Testing of CTs and PTs. Applications of CTs and PTs for the measurement of current, voltage, power and energy.	6
5	<b>Potentiometers:</b> Construction, operation and standardization of DC potentiometers- slide wire and Crompton potentiometers. Use of potentiometer for measurement of resistance and voltmeter and ammeter calibrations. Volt ratio boxes. Construction, operation and standardization of AC potentiometer in-phase and quadrature potentiometers. Applications of AC potentiometers.	5
6	<b>Measurement of Resistances:</b> Classification of resistance. Measurement of medium resistances - ammeter and voltmeter method, substitution method, Wheatstone bridge method. Measurement of low resistances - Potentiometer method and Kelvin's double bridge method. Measurement of high resistance: Price's Guard-wire method. Measurement of earth resistance.	6
7	<b>AC Bridges:</b> Generalized treatment of four-arm AC bridges. Sources and detectors. Maxwell's bridge, Hay's bridge and Anderson bridge for self-inductance measurement. Heaviside's bridge for mutual inductance measurement. De Sauty Bridge for capacitance measurement. Wien's bridge for capacitance and frequency measurements. Sources of error in bridge measurements and precautions. Screening of bridge components. Wagner earth device.	6
<b>Total</b>		<b>28</b>

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## SYLLABUS

2<sup>nd</sup> Year - IV Semester: B.Tech. (Electrical Engineering)

4EE4-05: Electrical Machines – II

Credit: 3  
3L+0T+0P

Max. Marks: 100(IA:30, ETE:70)  
End Term Exam: 3 Hours

SN	CONTENTS	Hours
1	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2	<b>Fundamentals of AC machine windings</b> Physical arrangement of windings in stator and cylindrical rotor; slots for windings; single turn coil - active portion and overhang; full-pitch coils, concentrated winding, distributed winding, winding axis, 3D visualization of the above winding types, Air-gap MMF distribution with fixed current through winding - concentrated and distributed, Sinusoidally distributed winding, winding distribution factor.	7
3	<b>Pulsating and revolving magnetic fields</b> Constant magnetic field, pulsating magnetic field - alternating current in windings with spatial displacement, Magnetic field produced by a single winding - fixed current and alternating current Pulsating fields produced by spatially displaced windings, Windings spatially shifted by 90 degrees, Addition of pulsating magnetic fields, Three windings spatially shifted by 120 degrees (carrying three-phase balanced currents), revolving magnetic field.	4
4	<b>Induction Machines</b> Construction, Types (squirrel cage and slip-ring), Torque Slip Characteristics, Starting and Maximum Torque. Equivalent circuit. Phasor Diagram, Losses and Efficiency. Effect of parameter variation on torque speed characteristics (variation of rotor and stator resistances, stator voltage, frequency). Methods of starting, braking and speed control for induction motors. Generator operation. Self-excitation. Doubly-Fed Induction Machines.	12
5	<b>Single-phase induction motors</b> Constructional features, double revolving field theory, equivalent circuit, determination of parameters. Split-phase starting methods and applications.	6
6	<b>Synchronous machines</b> Constructional features, cylindrical rotor synchronous machine - generated EMF, equivalent circuit and phasor diagram, armature reaction, synchronous impedance, voltage regulation. Operating characteristics of synchronous machines, V-curves. Salient pole machine – two reaction theory, analysis of phasor diagram, power angle characteristics. Parallel operation of alternators - synchronization and load division.	10
	<b>Total</b>	<b>40</b>

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2<sup>nd</sup> Year - IV Semester: B.Tech. (Electrical Engineering)

### 4EE4-06: Power Electronics

Credit: 3  
3L+0T+0P

Max. Marks: 100(IA:30, ETE:70)  
End Term Exam: 3 Hours

SN	CONTENTS	Hours
1	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2	<b>Power switching devices</b> Diode, Thyristor, MOSFET, IGBT: I-V Characteristics; Firing circuit for thyristor; Voltage and current commutation of a thyristor; Gate drive circuits for MOSFET and IGBT.	5
3	<b>Thyristor rectifiers</b> Single-phase half-wave and full-wave rectifiers, Single-phase full-bridge thyristor rectifier with R-load and highly inductive load; Three-phase full-bridge thyristor rectifier with R-load and highly inductive load; Input current wave shape and power factor.	6
4	<b>DC-DC buck converter</b> Elementary chopper with an active switch and diode, concepts of duty ratio and average voltage, power circuit of a buck converter, analysis and waveforms at steady state, duty ratio control of output voltage.	5
5	<b>DC-DC boost converter</b> Power circuit of a boost converter, analysis and waveforms at steady state, relation between duty ratio and average output voltage.	5
6	<b>Single-phase voltage source inverter</b> Power circuit of single-phase voltage source inverter, switch states and instantaneous output voltage, square wave operation of the inverter, concept of average voltage over a switching cycle, bipolar sinusoidal modulation and unipolar sinusoidal modulation, modulation index and output voltage.	10
7	<b>Three-phase voltage source inverter</b> Power circuit of a three-phase voltage source inverter, switch states, instantaneous output voltages, average output voltages over a sub-cycle, three-phase sinusoidal modulation.	8
	<b>Total</b>	<b>40</b>

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## SYLLABUS

2<sup>nd</sup> Year - IV Semester: B.Tech. (Electrical Engineering)

### 4EE4-07: Signals and Systems

Credit: 3  
3L+0T+0P

Max. Marks: 100(IA:30, ETE:70)  
End Term Exam: 3 Hours

SN	CONTENTS	Hours
1	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2	<b>Introduction to Signals and Systems:</b> Signals and systems as seen in everyday life, and in various branches of engineering and science. Signal properties: periodicity, absolute integrability, determinism and stochastic character. Some special signals of importance: the unit step, the unit impulse, the sinusoid, the complex exponential, some special time-limited signals; continuous and discrete time signals, continuous and discrete amplitude signals. System properties: linearity: additivity and homogeneity, shift-invariance, causality, stability, realizability. Examples.	6
3	<b>Behavior of continuous and discrete-time LTI systems:</b> Impulse response and step response, convolution, input-output behavior with aperiodic convergent inputs, cascade interconnections. Characterization of causality and stability of LTI systems. System representation through differential equations and difference equations. State-space Representation of systems. State-Space Analysis, Multi-input, multi-output representation. State Transition Matrix and its Role. Periodic inputs to an LTI system, the notion of a frequency response and its relation to the impulse response.	14
4	<b>Fourier, Laplace and z- Transforms:</b> Fourier series representation of periodic signals, Waveform Symmetries, Calculation of Fourier Coefficients. Fourier Transform, convolution/multiplication and their effect in the frequency domain, magnitude and phase response, Fourier domain duality. The Discrete-Time Fourier Transform (DTFT) and the Discrete Fourier Transform (DFT). Parseval's Theorem. Review of the Laplace Transform for continuous time signals and systems, system functions, poles and zeros of system functions and signals, Laplace domain analysis, solution to differential equations and system behavior. The z-Transform for discrete time signals and systems, system functions, poles and zeros of systems and sequences, z-domain analysis.	12
5	<b>Sampling and Reconstruction:</b> The Sampling Theorem and its implications. Spectra of sampled signals. Reconstruction: ideal interpolator, zero-order hold, first-order hold. Aliasing and its effects. Relation between continuous and discrete time systems. Introduction to the applications of signal and system theory: modulation for communication, filtering, feedback control systems.	8
<b>Total</b>		<b>41</b>

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## SYLLABUS

2<sup>nd</sup> Year - IV Semester: B.Tech. (Electrical Engineering)

### 4EE4-08: Digital Electronics

Credit: 2  
2L+0T+0P

Max. Marks: 100(IA:30, ETE:70)  
End Term Exam: 2 Hours

SN	CONTENTS	Hours
1	<b>Introduction:</b> Objective, scope and outcome of the course.	1
2	<b>Fundamentals of Digital Systems and logic families:</b> Digital signals, digital circuits, AND, OR, NOT, NAND, NOR and Exclusive-OR operations, Boolean algebra, examples of IC gates, number systems-binary, signed binary, octal hexadecimal number, binary arithmetic, one's and two's complements arithmetic, codes, error detecting and correcting codes, characteristics of digital ICs, digital logic families, TTL, Schottky TTL and CMOS logic, interfacing CMOS and TTL, Tri-state logic.	4
3	<b>Combinational Digital Circuits:</b> Standard representation for logic functions, K-map representation, simplification of logic functions using K-map, minimization of logical functions. Don't care conditions, Multiplexer, De-Multiplexer/Decoders, Adders, Subtractors, BCD arithmetic, carry look ahead adder, serial adder, ALU, elementary ALU design, popular MSI chips, digital comparator, parity checker/generator, code converters, priority encoders, decoders/drivers for display devices, Q-M method of function realization.	6
4	<b>Sequential circuits and systems:</b> A 1-bit memory, the circuit properties of Bistable latch, the clocked SR flip flop, J- K-T and D-types flip flops, applications of flip flops, shift registers, applications of shift registers, serial to parallel converter, parallel to serial converter, ring counter, sequence generator, ripple (Asynchronous) counters, synchronous counters, counters design using flip flops, special counter IC's, asynchronous sequential counters, applications of counters.	6
5	<b>A/D and D/A Converters:</b> Digital to analog converters: weighted resistor/converter, R-2R Ladder D/A converter, specifications for D/A converters, examples of D/A converter ICs, sample and hold circuit, analog to digital converters: quantization and encoding, parallel comparator A/D converter, successive approximation A/D converter, counting A/D converter, dual slope A/D converter, A/D converter using voltage to frequency and voltage to time conversion, specifications of A/D converters, example of A/D converter ICs	4
6	<b>Semiconductor memories and Programmable logic devices</b> Memory organization and operation, expanding memory size, classification and characteristics of memories, sequential memory, read only memory (ROM), read and write memory(RAM), content addressable memory (CAM), charge de coupled device memory (CCD), commonly used memory chips, ROM as a PLD, Programmable logic array, Programmable array logic, complex Programmable logic devices (CPLDS), Field Programmable Gate Array (FPGA).	7
<b>Office of Dean Academic Affairs</b>		<b>Total</b>
<b>Rajasthan Technical University, Kota</b>		<b>28</b>



# RAJASTHAN TECHNICAL UNIVERSITY, KOTA

## SYLLABUS

2<sup>nd</sup> Year - IV Semester: B.Tech. (Electrical Engineering)

**4EE4-21: Electrical Machines - II Lab**

**Credit: 2**  
**OL+OT+4P**

**Max. Marks: 100(IA:60, ETE:40)**

- 1) To study various types of starters used for 3 phase induction motor.
- 2) To connect two 3-phase induction motor in cascade and study their speed control.
- 3) To perform load test on 3-phase induction motor and calculate torque, output power, input power, efficiency, input power factor and slip for various load settings.
- 4) To perform no load and blocked rotor test on a 3-phase induction motor and determine the parameters of its equivalent circuits.
- 5) Draw the circle diagram and compute the following (i) Max. Torque (ii) Current (iii) slips (iv) p. f. (v) Efficiency.
- 6) Speed control of 3-  $\Phi$  Induction Motor
- 7) To plot the O.C.C. & S.C.C. of an alternator.
- 8) To determine  $Z_s$ ,  $X_d$  and  $X_q$  by slip test, Zero power factor (ZPF)/ Potier reactance method.
- 9) To determine the voltage regulation of a 3-phase alternator by direct loading.
- 10) To determine the voltage regulation of a 3-phase alternator by synchronous impedance method.
- 11) To study effect of variation of field current upon the stator current and power factor of synchronous motor and Plot V-Curve and inverted V-Curve of synchronous motor for different values of loads.
- 12) To synchronize an alternator across the infinite bus and control load sharing.

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2<sup>nd</sup> Year - IV Semester: B.Tech. (Electrical Engineering)

### 4EE4-22: Power Electronics Lab

**Credit: 2**  
**OL+OT+4P**

**Max. Marks: 100(IA:60, ETE:40)**

- 1) Study the comparison of following power electronics devices regarding ratings, performance characteristics and applications: Power Diode, Power Transistor, Thyristor, Diac, Triac, GTO, MOSFET, MCT and SIT.
- 2) Determine V-I characteristics of SCR and measure forward breakdown voltage, latching and holding currents.
- 3) Find V-I characteristics of TRIAC and DIAC.
- 4) Find output characteristics of MOSFET and IGBT.
- 5) Find transfer characteristics of MOSFET and IGBT.
- 6) Find UJT static emitter characteristics and study the variation in peak point and valley point.
- 7) Study and test firing circuits for SCR-R, RC and UJT firing circuits.
- 8) Study and test 3-phase diode bridge rectifier with R and RL loads. Study the effect of filters.
- 9) Study and obtain waveforms of single-phase half wave controlled rectifier with and without filters. Study the variation of output voltage with respect to firing angle.
- 10) Study and obtain waveforms of single-phase half controlled bridge rectifier with R and RL loads. Study and show the effect of freewheeling diode.
- 11) Study and obtain waveforms of single-phase full controlled bridge converter with R and RL loads. Study and show rectification and inversion operations with and without freewheeling diode.
- 12) Control the speed of a dc motor using single-phase half controlled bridge rectifier and full controlled bridge rectifier. Plot armature voltage versus speed characteristics.

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### 4EE4-23: Digital Electronics Lab

**Credit: 1**  
**OL+OT+2P**

**Max. Marks: 100(IA:60, ETE:40)**

- 1) To verify the truth tables of basic logic gates: AND, OR, NOR, NAND, NOR. Also to verify the truth table of Ex-OR, Ex-NOR (For 2, 3, & 4 inputs using gates with 2, 3, & 4 inputs).
- 2) To verify the truth table of OR, AND, NOR, Ex-OR, Ex-NOR realized using NAND & NOR gates.
- 3) To realize an SOP and POS expression.
- 4) To realize Half adder/ Subtractor & Full Adder/ Subtractor using NAND & NOR gates and to verify their truth tables.
- 5) To realize a 4-bit ripple adder/ Subtractor using basic half adder/ Subtractor & basic Full Adder/ Subtractor.
- 6) To verify the truth table of 4-to-1 multiplexer and 1-to-4 demultiplexer. Realize the multiplexer using basic gates only. Also to construct an 8-to-1 multiplexer and 1-to-8 demultiplexer using blocks of 4-to-1 multiplexer and 1-to-4 demultiplexer.
- 7) Design & Realize a combinational circuit that will accept a 2421 BCD code and drive a TIL -312 seven segment display.
- 8) Using basic logic gates, realize the R-S, J-K and D-flip flops with and without clock signal and verify their truth table.
- 9) Construct a divide by 2, 4 & 8 asynchronous counter. Construct a 4-bit binary counter and ring counter for a particular output pattern using D flip flop.
- 10) Perform input/output operations on parallel in/Parallel out and Serial in/Serial out registers using clock. Also exercise loading only one of multiple values into the register using multiplexer.

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2<sup>nd</sup> Year - IV Semester: B.Tech. (Electrical Engineering)

### 4EE4-24: Measurement Lab

**Credit: 1**  
**OL+OT+2P**

**Max. Marks: 100(IA:60, ETE:40)**

- 1) Study working and applications of (i) C.R.O. (ii) Digital Storage C.R.O. & (iii) C.R.O. Probes.
- 2) Study working and applications of Meggar, Tong-tester, P.F. Meter and Phase Shifter.
- 3) Measure power and power factor in 3-phase load by (i) Two-wattmeter method and (ii) One-wattmeter method.
- 4) Calibrate an ammeter using DC slide wire potentiometer.
- 5) Calibrate a voltmeter using Crompton potentiometer.
- 6) Measure low resistance by Crompton potentiometer.
- 7) Measure Low resistance by Kelvin's double bridge.
- 8) Measure earth resistance using fall of potential method.
- 9) Calibrate a single-phase energy meter by phantom loading at different power factors.
- 10) Measure self-inductance using Anderson's bridge.