

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2015

Time Table – II Year (III Sem)

DAY	08:00 - 09:00	09:00- 10:00	10:00- 11:00	11:00- 12:00	12:00- 12:30	12:30- 1:30	1:30 to 2:30	2:30- 3:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	OOPS	AEM	EDC	CA-I/CAS	Lunch	Humanities & Social Sciences		LIB	Electrical Circuit Analysis
TUES	OOPS	AEM	EDC	CA-I/CAS	Lunch	OOPS	EDC	LIB	Electrical Circuit Analysis
WED	OOPS	AEM	EDC	CA-I/CAS	Lunch	C++ Prog lab ----A1 CA Lab-----A2			Electrical Circuit Analysis
THURS	DE	AEM	EDC	DE	Lunch	C++ Prog lab ---A2 CA Lab-----A1			Electrical Circuit Analysis
FRI	EM-I	EM-I	DE	DE	Lunch	EDC Lab			Electrical Circuit Analysis
SATUR	EM-I	EM-I	CA-I/CAS	DE	Lunch	DE Lab			Electrical Circuit Analysis

Subject

Faculty

OOPS

Pankaj Ameta

CA Lab

Ashika Sharma

AEM

Kirti Kurdiya

Humanities & Social Sciences CPJain

EDC

Yashwant soni

CA-I

Ashika Sharma

CAS

Ashika Sharma

DE

Ashok K.

EM-I

Prakash Sundram

EDC Lab

Yashwant soni

DE Lab

Yashwant soni

C++ Lab

Pankaj Ameta

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY
पंकज पौरवाल

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2015

Time Table – IV Year (VIII Sem)

DAY	08:00 -9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-12:30	12:30-03:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	EDTC	EDTC	PPS	EHV	L U N C H	SEMINAR	Electric Drives And Their Control
TUES	UEP	EDTC	PPS	EHV		SEMINAR	Electric Drives And Their Control
WED	EDTC	UEP	PPS	EHV		ED & C LAB A1 CBPS LAB A2	Electric Drives And Their Control
THURS	EDTC	UEP	PPS	UEP		ED & C LAB A2 CBPS LAB A1	Electric Drives And Their Control
FRI	UEP	EHV	PPS	EHV		MAJOR PROJECT	Electric Drives And Their Control
SATUR	UEP	PPS	EHV	EDTC		MAJOR PROJECT	Electric Drives And Their Control

Subject

EDTC

EHV AC/ DC

PPS

UEP

EDTC Lab

CBPS Lab

Faculty

Raju Swami

Abrar Ahmed

Abrar Ahmed

CPJain

Raju Swami

Raju Swami

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY
2015

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2015

Time Table – IV Year (VII Sem)

DAY	10:00-11:00	11:00-12:00	12:00 to 1:00	1:00 to 2:00	2:00-2:30	2:30-03:20	03:20-04:10	04:10-05:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 2:30 to 5:00
MON	NCES	Power System Modeling-----A1 Power system Planning -----A2			Lunch	PSA	PSE	OS/EOPS	Electric Drives And Their Control
TUES	OS/EOPS	PSE	NCES	AIT	Lunch	PSP	PSA	AIT	Electric Drives And Their Control
WED	PSA	PSE	AIT	OS/EOPS	Lunch	PSP	AIT	NCES	Electric Drives And Their Control
THURS	NCES	PSE	Power System Modeling-----A2 Power system Planning -----A1			Lunch	PSP	PSA	Electric Drives And Their Control
FRI	OS/EOPS	PSP	PSE	AIT	Lunch	Project			Electric Drives And Their Control
SATUR	OS/EOPS	PSP	NCES	PSA	Lunch	Seminar /Industrial Economics & Management Lab			Electric Drives And Their Control

Subject

NCES

PSP

PSA

PSE

AIT

OS

EOPS

Power System Modelling Lab

Power system Planning Lab

Industrial Economics & Management Lab

Faculty

Ashika Sharma

CPJain

CPJain

Irfan Ali

Kirti

Gaurav Kumawat

Irfan Ali

Irfan Ali

CPJain

CPJain

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY.
के.के. जैवाल

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2015

Time Table – III Year (V Sem)

DAY	8:00 to 9:00	9:00 to 10:00	10:00-11:00	11:00-12:00	12:00 - 12:30	12:30 to 1:20	1:20 to 2:10	2:00 to 3:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	T&D	T&D	CS	CS	Lunch	PE	PE	PE	Electric Drives
TUES	T&D	PE	CS	MP/VLSI		Comm Lab/MP Lab			Electric Drives
WED	DBMS	T&D	T&D	MP/VLSI		PCS			Electric Drives
THURS	DBMS	DBMS	PCS	MP/VLSI		SP Lab			Electric Drives
FRI	DBMS	T&D	T&D	MP/VLSI		CS	PCS	PCS	Electric Drives
SATUR	PE	PCS	PCS	CS		PE Lab / Professional Ethics and Disaster Management			Electric Drives

Subject

T&D

CS

MP

VLSI

PE

PCS

Comm Lab

PE Lab

Professional Ethics and

Disaster Management

SP Lab

DBMS

Faculty

CPJain

Prof. Rajesh Purohit

Vivek Jain

Yogendra Solanki

Abrar Ahmed

Richa Jani

Richa Jani

Abrar Ahmed

CPJain

CPJain

Pankaj

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY
पंजाब विश्वविद्यालय

Techno India NJR Institute of Technology
Department of Electrical Engineering (EE & EEE)

Session: 2015

Time Table – III Year (VI Sem)

DAY	08:00 - 09:00	09:00-10:00	10:00-11:00	11:00-12:00	12:00-12:30	12:30 to 1:30	01:30-2:15		After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	SGT	APE	HVE/NN	AMP/MPMC	L U N C H	APE	HVE/NN	SGT	Electric Drives
TUES	SGP	SGT	HVE/NN	AMP/MPMC		Smart Grid Lab			Electric Drives
WED	MCT	SGP	AMP/MPMC	APE		Control system LAB -- A1 APE Lab ---- A2			Electric Drives
THURS	SGP	SGT	HVE/NN	MCT		PS Lab/MPMC Lab			Electric Drives
FRI	SGP	MCT	HVE/NN	AMP/MPMC		APE Lab ---- A1 Control System Lab A2			Electric Drives
SATUR	MCT	SGT	APE	APE		AMP/MPMC	Entrepreneurship Development		Electric Drives

Subject

MCT

MPMC

SGP

HVE

NN

AMP

APE

SGT

Smart Grid Lab

Control system LAB

Faculty

CPJain

Yogendra Solanki

Abrar Ahmed

Raju Swami

Gaurav Ameta

Yogendra Solanki

Raju Swami

Divya Jain

Divya Jain

CPJain

Subject

APE Lab

Entrepreneurship Development

MPMC Lab

PS Lab

Faculty

Raju Swami

CPJain

Yogendra Solanki

Pushkar Lohar

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY
पंकज वैखान

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2015

Time Table – II Year (IV Sem EE & EEE)

DAY	08:00 - 09:00	09:00- 10:00	10:00- 11:00	11:00- 12:00	12:00- 12:30	12:30 to 1:20	01:20- 2:10	02:10- 03:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	EEM/CA-II	LIC/GEP	AE	EM	L U N C H	AEM-II	IC Lab /PSD Lab		Electrical Circuit Analysis
TUES	AE	LIC/GEP	EEM	EEM/CA-II		AEM-II	LIC/GEP	EM	Electrical Circuit Analysis
WED	LIC/GEP	AE	EM	EM		Electrical Measurement Lab			Electrical Circuit Analysis
THURS	LIC/GEP	EM	EEM/CA-II	AE		Analog Electronic Lab			Electrical Circuit Analysis
FRI	EM-II	EM-II	EEM/CA-II	AEM-II		Electrical machine design Lab			Electrical Circuit Analysis
SATUR	EM-II	EM-II	AEM-II	AE		Electrical Machine lab			Electrical Circuit Analysis

Subject

EEM
LIC
AE
GEP
EM-II
CA-II
EM
AEM-II
E Measurement Lab
EMD Lab

Faculty

Tanu
Yashwant
Nitin Kothari
Irfan Ali
Amrit Lal
Aashika
Aashika
Gajandra Purohit
Ashika
Pushkar Lohar

Subject

Electrical Machine lab
Analog Electronic Lab
IC Lab
PSD Lab

Faculty

Pushkar Lohar
Nitin Kothari
Yashwant Soni
Bheru Lal

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

पंकज पोरवाल

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2016

Time Table – II Year (IV Sem EE & EEE)

DAY	08:00 - 09:00	09:00- 10:00	10:00- 11:00	11:00- 12:00	12:00- 12:30	12:30 to 1:20	01:20- 2:10	02:10- 03:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	EEM/CA-II	LIC/GEP	AE	EM	L U N C H	AEM-II	IC Lab /PSD Lab		Electrical Circuit Analysis
TUES	AE	LIC/GEP	EEM	EEM/CA-II		AEM-II	LIC/GEP	EM	Electrical Circuit Analysis
WED	LIC/GEP	AE	EM	EM		Electrical Measurement Lab			Electrical Circuit Analysis
THURS	LIC/GEP	EM	EEM/CA-II	AE		Analog Electronic Lab			Electrical Circuit Analysis
FRI	EM-II	EM-II	EEM/CA-II	AEM-II		Electrical machine design Lab			Electrical Circuit Analysis
SATUR	EM-II	EM-II	AEM-II	AE		Electrical Machine lab			Electrical Circuit Analysis

Subject

EEM
LIC
AE
GEP
EM-II
CA-II
EM
AEM-II
E Measurement Lab
EMD Lab

Faculty

Tanu
Yashwant
Nitin Kothari
Irfan Ali
Amrit Lal
Aashika
Aashika
Gajandra Purohit
Ashika
Pushkar Lohar

Subject

Electrical Machine lab
Analog Electronic Lab
IC Lab
PSD Lab

Faculty

Pushkar Lohar
Nitin Kothari
Yashwant Soni
Bheru Lal

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY



Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2016

Time Table – III Year (VI Sem)

DAY	08:00 - 09:00	09:00-10:00	10:00-11:00	11:00-12:00	12:00-12:30	12:30 to 1:30	01:30-2:15	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	SGT	APE	HVE/NN	AMP/MPMC	L U N C H	APE	HVE/NN SGT	Electric Drives
TUES	SGP	SGT	HVE/NN	AMP/MPMC		Smart Grid Lab		Electric Drives
WED	MCT	SGP	AMP/MPMC	APE		Control system LAB -- A1 APE Lab ---- A2		Electric Drives
THURS	SGP	SGT	HVE/NN	MCT		PS Lab/MPMC Lab		Electric Drives
FRI	SGP	MCT	HVE/NN	AMP/MPMC		APE Lab ---- A1 Control System Lab A2		Electric Drives
SATUR	MCT	SGT	APE	APE		AMP/MPMC	Entrepreneurship Development	Electric Drives

Subject

MCT
MPMC
SGP
HVE
NN
AMP
APE
SGT
Smart Grid Lab
Control system LAB

Faculty

CPJain
Yogendra Solanki
Abrar Ahmed
Raju Swami
Gaurav Ameta
Yogendra Solanki
Raju Swami
Divya Jain
Divya Jain
CPJain

Subject

APE Lab
Entrepreneurship Development
MPMC Lab
PS Lab

Faculty

Raju Swami
CPJain
Yogendra Solanki
Pushkar Lohar

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY
2016-2017

Techno India NJR Institute of Technology
Department of Electrical Engineering (EE & EEE)

Session: 2016

Time Table – IV Year (VIII Sem)

DAY	08:00 -9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-12:30	12:30-03:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	EDTC	EDTC	PPS	EHV	L U N C H	SEMINAR	Electric Drives And Their Control
TUES	UEP	EDTC	PPS	EHV		SEMINAR	Electric Drives And Their Control
WED	EDTC	UEP	PPS	EHV		ED & C LAB A1 CBPS LAB A2	Electric Drives And Their Control
THURS	EDTC	UEP	PPS	UEP		ED & C LAB A2 CBPS LAB A1	Electric Drives And Their Control
FRI	UEP	EHV	PPS	EHV		MAJOR PROJECT	Electric Drives And Their Control
SATUR	UEP	PPS	EHV	EDTC		MAJOR PROJECT	Electric Drives And Their Control

Subject

EDTC
 EHV AC/ DC
 PPS
 UEP
 EDTC Lab
 CBPS Lab

Faculty

Raju Swami
 Abrar Ahmed
 Abrar Ahmed
 CPJain
 Raju Swami
 Raju Swami

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY
 2016

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2016

Time Table – II Year (III Sem)

DAY	08:00 - 09:00	09:00- 10:00	10:00- 11:00	11:00- 12:00	12:00- 12:30	12:30- 1:30	1:30 to 2:30	2:30- 3:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	OOPS	AEM	EDC	CA-I/CAS	Lunch	Humanities & Social Sciences		LIB	Electrical Circuit Analysis
TUES	OOPS	AEM	EDC	CA-I/CAS	Lunch	OOPS	EDC	LIB	Electrical Circuit Analysis
WED	OOPS	AEM	EDC	CA-I/CAS	Lunch	C++ Prog lab ----A1 CA Lab-----A2			Electrical Circuit Analysis
THURS	DE	AEM	EDC	DE	Lunch	C++ Prog lab ---A2 CA Lab-----A1			Electrical Circuit Analysis
FRI	EM-I	EM-I	DE	DE	Lunch	EDC Lab			Electrical Circuit Analysis
SATUR	EM-I	EM-I	CA-I/CAS	DE	Lunch	DE Lab			Electrical Circuit Analysis

Subject

Faculty

OOPS

Pankaj Ameta

CA Lab

Ashika Sharma

AEM

Kirti Kurdiya

Humanities & Social Sciences CPJain

EDC

Yashwant soni

CA-I

Ashika Sharma

CAS

Ashika Sharma

DE

Ashok K.

EM-I

Prakash Sundram

EDC Lab

Yashwant soni

DE Lab

Yashwant soni

C++ Lab

Pankaj Ameta

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2016

Time Table – III Year (V Sem)

DAY	8:00 to 9:00	9:00 to 10:00	10:00-11:00	11:00-12:00	12:00 - 12:30	12:30 to 1:20	1:20 to 2:10	2:00 to 3:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	T&D	T&D	CS	CS	Lunch	PE	PE	PE	Electric Drives
TUES	T&D	PE	CS	MP/VLSI		Comm Lab/MP Lab			Electric Drives
WED	DBMS	T&D	T&D	MP/VLSI		PCS			Electric Drives
THURS	DBMS	DBMS	PCS	MP/VLSI		SP Lab			Electric Drives
FRI	DBMS	T&D	T&D	MP/VLSI		CS	PCS	PCS	Electric Drives
SATUR	PE	PCS	PCS	CS		PE Lab / Professional Ethics and Disaster Management			Electric Drives

Subject

T&D

CS

MP

VLSI

PE

PCS

Comm Lab

PE Lab

Professional Ethics and

Disaster Management

SP Lab

DBMS

Faculty

CPJain

Prof. Rajesh Purohit

Vivek Jain

Yogendra Solanki

Abrar Ahmed

Richa Jani

Richa Jani

Abrar Ahmed

CPJain

CPJain

Pankaj

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

[Signature]

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2016

Time Table – IV Year (VII Sem)

DAY	10:00-11:00	11:00-12:00	12:00 to 1:00	1:00 to 2:00	2:00-2:30	2:30-03:20	03:20-04:10	04:10-05:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 2:30 to 5:00
MON	NCES	Power System Modeling-----A1 Power system Planning -----A2			Lunch	PSA	PSE	OS/EOPS	Electric Drives And Their Control
TUES	OS/EOPS	PSE	NCES	AIT	Lunch	PSP	PSA	AIT	Electric Drives And Their Control
WED	PSA	PSE	AIT	OS/EOPS	Lunch	PSP	AIT	NCES	Electric Drives And Their Control
THURS	NCES	PSE	Power System Modeling-----A2 Power system Planning -----A1			Lunch	PSP	PSA	Electric Drives And Their Control
FRI	OS/EOPS	PSP	PSE	AIT	Lunch	Project			Electric Drives And Their Control
SATUR	OS/EOPS	PSP	NCES	PSA	Lunch	Seminar /Industrial Economics & Management Lab			Electric Drives And Their Control

Subject

NCES

PSP

PSA

PSE

AIT

OS

EOPS

Power System Modelling Lab

Power system Planning Lab

Industrial Economics & Management Lab

Faculty

Ashika Sharma

CPJain

CPJain

Irfan Ali

Kirti

Gaurav Kumawat

Irfan Ali

Irfan Ali

CPJain

CPJain

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2017

Time Table – II Year (IV Sem)

DAY	08:00 - 09:00	09:00- 10:00	10:00- 11:00	11:00- 12:00	12:00- 12:30	12:30 to 1:20	01:20- 2:10	02:10- 03:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	AE	AE	PSD Lab/IC Lab			Lunch	GEP \LIC	GEP \LIC	Electrical Circuit Analysis
TUES	CA-II/EEM	AE	EMD Lab				AE	EM	Electrical Circuit Analysis
WED	EM-II	EM-II	AEM-II	AE	Lunch	EM Lab			Electrical Circuit Analysis
THURS	EM-II	EM-II	EM-II	EM		Electrical Machine lab			Electrical Circuit Analysis
FRI	GEP \LIC	GEP \LIC	AEM-II	AEM-II		CA-II/EEM	CA-II/EEM	EM	Electrical Circuit Analysis
SATUR	EM	AEM-II	AEM-II	CA-II/EEM		AE Lab			Electrical Circuit Analysis

Subject

EEM

LIC

AE

GEP

EM-II

CA-II

EM

AEM-II

Electrical Measurement Lab

Electrical machine design Lab

Faculty

Tanu

Yashwant

Nitin Kothari

Janti Lal

Amrit lal Jangid

Aashika

Aashika

Kirti Kurdiya

Ashika

Pushkar Lohar

Electrical Machine lab Pushkar Lohar

Analog Electronic Lab Nitin Kothari

IC Lab Yashwant

PSD Lab Bheru Lal

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

2017-2018

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2017

Time Table – III Year (VI Sem)

DAY	08:00 - 09:00	09:00- 10:00	10:00- 11:00	11:00-12:00	12:00- 12:30	12:30 to 1:30	01:30- 2:30	02:30- 03:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	APE	APE	SGP	MCT		APE Lab A1 Batch Control System Lab A2			Electric Drives
TUES	MCT	APE	APE	SGT		APE Lab A2 Batch Control System Lab A1			Electric Drives
WED	SGT	MCT	HVE/NN	HVE/NN		SGP	APE	SGT	Electric Drives
THURS	Smart grid lab /MPMC Lab			AMP/MPMC		SGP	SGT	LIB	Electric Drives
FRI	SGP	AMP/MPMC	HVE/NN	SGT		Power system lab & Entrepreneurship Development			Electric Drives
SATUR	AMP/MPMC	AMP/MPMC	HVE/NN	HVE/NN		SGP	MCT	MCT	Electric Drives

Subject

MCT

MPMC

SGP

HVE

NN

AMP

APE

Faculty

CPJain

Yogendra Solanki

Gajendra Sharma

Jayanti Lal

Kirti

Vivek Jain

Abrar Ahmed

SGT

Smart Grid Lab

Control system LAB

APE Lab

Entrepreneurship Development

MPMC Lab

PS Lab

Ashika Sharma

Ashika Sharma

CPJain

Abrar Ahmed

CPJain

Yogendra Solanki

Pushkar Lohar

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2017

Time Table – IV Year (VIII Sem)

DAY	10:00-11:00	11:00-12:00	12:00-1:00	1:00-2:00	2:00-2:30	2:30-05:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 2:30 to 5:00
MON	EDTC	EHV	PPS	PPS	L U N C H	EDTC Lab	Electric Drives And Their Control
TUES	UEP	EDTC	EHV	PPS		HV Lab	Electric Drives And Their Control
WED	EDTC	EDTC	EHV	EHV		CBPS Lab	Electric Drives And Their Control
THURS	EDTC	EDTC	EHV	EHV		Seminar	Electric Drives And Their Control
FRI	PPS	PPS	UEP	UEP		Project /Activity	Electric Drives And Their Control
SATUR	UEP	PPS	UEP	PPS		Project /Activity	Electric Drives And Their Control

Subject

EDTC

EHV AC/ DC

PPS

UEP

EDTC Lab

CBPS Lab

HV Lab

Faculty

Abrar Ahmed

CPJain

Raj kumar

CPJain

Abrar Ahmed

Rajkumar

CPJain

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

2017-2018

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2017

Time Table – II Year (III Sem)

DAY	08:00 - 09:00	09:00- 10:00	10:00- 11:00	11:00- 12:00	12:00- 12:30	12:30 to 01:20	1:20 to 2:10	2:10 to 3:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	EDC	CA-I/ CAS	EM-I	OOPS	Lunch	DE Lab			Electrical Circuit Analysis
TUES	CA-I/ CAS	EDC	EM-I	OOPS		CA-I/ CAS	DE	LIB	Electrical Circuit Analysis
WED	CA-I/ CAS	EDC	AEM-I	AEM-I		Humanities & Social Sciences			Electrical Circuit Analysis
THURS	DE	AEM-I	EM-I	EM-I		Circuit Analysis Lab			Electrical Circuit Analysis
FRI	EDC	CA-I/ CAS	AEM-I	EM-I		OOPS			Electrical Circuit Analysis
SATUR	EM-I	EDC Lab				CA-I/ CAS	EDC	EDC	Electrical Circuit Analysis

Subject

Faculty

OOPS

Pankaj Ameta

AEM

Kirti Kurdiya

EDC

Yashwant soni

CA-I

Ashika Sharma

CAS

Ashika Sharma

DE

Yogendra Solanki

EM-I

Lalita

EDC Lab

Yashwant soni

DE Lab

Yogendra Solanki

C++ Lab

Pankaj Ameta

CA Lab

Ashika Sharma

Humanities & Social Sciences

CPJain

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

Signature

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2017

Time Table – III Year (V Sem)

DAY	10:00-11:00	11:00-12:00	12:00 - 1:00	1:00 - 2:00	2:00-2:30	2:30-03:20	03:20-04:10	04:10-05:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 2:30 to 5:00
MON	DBMS	DBMS	Lunch	PCS			SP Lab		Electric Drives
TUES	VLSI /MP	PE	PE	T & D	LUNCH	CS	CS	PCS	Electric Drives
WED	VLSI /MP	PCS	PE	PE	LUNCH	CS	CS	PCS	Electric Drives
THURS	T & D	T & D	Lunch	Professional Ethics and Disaster Management			VLSI /MP	DBMS	Electric Drives
FRI	T & D	T & D		PCS 12:30 to 3:01			PE	PE	Electric Drives
SATUR	DBMS	DBMS	PE	PCS	LUNCH	DBMS Lab			Electric Drives

Subject

T&D

CS

MP

VLSI

PE

PCS

Comm Lab

PE Lab

Professional Ethics and

Disaster Management

Faculty

Neha Tak

CPJain

Vivek Jain

Yogendra Solanki

Neha Tak

Vivek Jain

Yashwant Soni

Abrar Ahmed

CPJain

SP Lab

DBMS

DBMS Lab

CPJain

Poornima Dave

Poornima Dave

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

Signature

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2017

Time Table – IV Year (VII Sem)

DAY	10:00-11:00	11:00-12:00	12:00 -1:00	1:00 -2:00	2:00-2:30	2:30-03:20	03:20-04:10	04:10-05:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 5:00
MON	AIT	PSP	PSA	EOPS /OS	Lunch	Power System Modeling-----A1 Power system Planning -----A2			Electric Drives and Their Control
TUES	EOPS /OS	PSE	PSP	AIT		PSP	NCES	PSE	Electric Drives and Their Control
WED	EOPS /OS	NCES	PSP	AIT		Power System Modeling-----A2 Power system Planning -----A1			Electric Drives and Their Control
THURS	AIT	EOPS /OS	EOPS /OS	NCES		PSE	PSA	PSA	Electric Drives and Their Control
FRI	PSP	PSE	PSA	NCES		Project			Electric Drives and Their Control
SATUR	AIT	NCES	PSA	PSE		Seminar / Industrial Economics & Management			Electric Drives and Their Control

Subject

NCES

PSP

PSA

PSE

AIT

OS

EOPS

Power System Modelling Lab

Power system Planning Lab

Industrial Economics & Management Lab

Faculty

Ashika Sharma

Lalita

CPJain

Irfan Ali

Poornima Dave

Gaurav Kumawat

Rajkumar Soni

Rajkumar Soni

CPJain

CPJain

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY
2017-18

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2018

Time Table – II Year (IV Sem)

DAY	08:00 - 09:00	09:00- 10:00	10:00- 11:00	11:00- 12:00	12:00- 12:30	12:30 to 1:20	01:20- 2:10	02;10- 03:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	AE	AE	PSD Lab/IC Lab			Lunch	GEP \LIC	GEP \LIC	Electrical Circuit Analysis
TUES	CA-II/EEM	AE	EMD Lab				AE	EM	Electrical Circuit Analysis
WED	EM-II	EM-II	AEM-II	AE	Lunch	EM Lab			Electrical Circuit Analysis
THURS	EM-II	EM-II	EM-II	EM		Electrical Machine lab			Electrical Circuit Analysis
FRI	GEP \LIC	GEP \LIC	AEM-II	AEM-II		CA-II/EEM	CA-II/EEM	EM	Electrical Circuit Analysis
SATUR	EM	AEM-II	AEM-II	CA-II/EEM		AE Lab			Electrical Circuit Analysis

Subject

EEM

LIC

AE

GEP

EM-II

CA-II

EM

AEM-II

Electrical Measurement Lab

Electrical machine design Lab

Faculty

Tanu

Yashwant

Nitin Kothari

Janti Lal

Amrit lal Jangid

Aashika

Aashika

Kirti Kurdiya

Ashika

Pushkar Lohar

Electrical Machine lab

Analog Electronic Lab

IC Lab

PSD Lab

Pushkar Lohar

Nitin Kothari

Yashwant

Bheru Lal

10/11/2018
10:00 AM
10/11/2018
10:00 AM

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2018

Time Table – III Year (VI Sem)

DAY	08:00 - 09:00	09:00- 10:00	10:00- 11:00	11:00- 12:00	12:00- 12:30	12:30 to 1:20	01:20-2:10	02:10- 03:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	SGT	APE	MCT	HVE/NN		PS Lab / Entrepreneurship Development			Electric Drives
TUES	APE	APE	MCT	HVE/NN		APE Lab A2 Batch Control System Lab A1			Electric Drives
WED	APE	APE	MCT	HVE/NN		AMP/MPMC	AMP/MPMC	SGP	Electric Drives
THURS	HVE/NN	MCT	MCT	SGT		AMP/MPMC	SGP		Electric Drives
FRI	HVE/NN	AMP/MPMC	AMP/MPMC	SGT		Smart grid lab /MPMC Lab			Electric Drives
SATUR	SGT	SGT	SGP	SGP		APE Lab A1 Batch Control System Lab A2			Electric Drives

Subject

MCT
MPMC
SGP
HVE
NN
AMP
APE
SGT
Smart Grid Lab
Control system LAB
APE Lab

Faculty

CPJain
Yogendra Solanki
Abrar Ahmed
Lalita
Kirti
Vivek Jain
Neha Tak
Ashika
Ashika
CPJain
Neha Tak

Entrepreneurship Development
MPMC Lab
PS Lab

CPJain
Yogendra Solanki
Pushkar Lohar

[Handwritten signature and stamp]

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2018

Time Table – IV Year (VIII Sem)

DAY	10:00-11:00	11:00-12:00	12:00-1:00	1:00-2:00	2:00-2:30	2:30-05:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 2:30 to 5:00
MON	EHV	PPS	PPS	UEP		EDTC EDTC	Electric Drives and Their Control
TUES	EHV	PPS	CBPS Lab			EDTC EDTC	Electric Drives and Their Control
WED	EDTC lab		UEP	UEP		HV Lab	Electric Drives And Their Control
THURS	EHV	PPS	UEP	UEP		Seminar	Electric Drives And Their Control
FRI	EHV	PPS	UEP	EHV		Project /Activity	Electric Drives And Their Control
SATUR	PPS	EHV	EDTC	EDTC		Project /Activity	Electric Drives And Their Control

Subject

EDTC

EHV AC/ DC

PPS

UEP

EDTC Lab

CBPS Lab

HV Lab

Faculty

Abrar Ahmed

CPJain

Raj kumar

CPJain

Abrar Ahmed

Rajkumar

CPJain

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

Handwritten signature

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2018

Time Table – II Year (III Sem)

DAY	08:00 - 09:00	09:00- 10:00	10:00- 11:00	11:00- 12:00	12:00- 12:30	12:30- 1:20	1:20 to 2:10	2:10- 3:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	EM-I	EM-I	EMFT	PGP		PGP	ECA	ECA	Electrical Circuit Analysis
TUES	EM-I	EM-I	AE	ECA		Electrical Circuit Design Lab			Electrical Circuit Analysis
WED	AE	AEM-I	TC	PGP		EMFT		EMFT	Electrical Circuit Analysis
THURS	AE	AEM-I	TC	ECA		EMFT		TC	Electrical Circuit Analysis
FRI	AE	AEM-I	ECA	PGP		EM- I Lab			Electrical Circuit Analysis
SATUR	AE	EM-I	TC	EMFT		AE Lab			Electrical Circuit Analysis

Subject

EM-I

EMFT

PGP

ECA

AE

AEM

TC

AE Lab

Electrical Circuit Design Lab

EM- I Lab

Faculty

Lalita

CPJain

CPJain

Ashika

Nitin kothari

Dr. Payal Jain

Hitkaran Singh Ranawat

Nitin kothari

Rajkumar

Lalita

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

Signature

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2018

Time Table – III Year (V Sem)

DAY	8:00 to 9:00	9:00 to 10:00	10:00-11:00	11:00-12:00	12:00 - 12:30	12:30 to 1:20	1:20 to 2:10	2:00 to 3:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	T&D	T&D	CS	CS	Lunch	PE	PE	PE	Electric Drives
TUES	T&D	PE	CS	MP/VL SI		Comm Lab/MP Lab			Electric Drives
WED	DBMS	T&D	T&D	MP/VL SI		DBMS Lab			Electric Drives
THURS	DBMS	DBMS	PCS	MP/VL SI		CS			Electric Drives
FRI	DBMS	T&D	T&D	MP/VL SI		CS	PCS	PCS	Electric Drives
SATUR	PE	PCS	PCS	CS		PE Lab / Professional Ethics and Disaster Management			Electric Drives

Subject

T&D

CS

MP

VLSI

PE

PCS

Comm Lab

PE Lab

Faculty

Zenab

CPJain

Vivek Jain

Yogendra Solanki

Neha Tak

Vivek Jain

Yashwant Soni

Abrar Ahmed

Professional Ethics and Disaster Management

SP Lab

DBMS

DBMS Lab

CPJain

CPJain

Pankaj

Pankaj

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

10/07/2018

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2018

Time Table – IV Year (VII Sem)

DAY	8:00 to 9:00	9:00 to 10:00	10:00-11:00	11:00-12:00	12:00-12:30	12:30- to 01:20	01:20- 02:10	02:10- 03:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	AIT	PSE	EOPS /OS	EOPS /OS	Lunch	Power system Planning			Electric Drives And Their Control
TUES	AIT	NCES	PSE	EOPS /OS		PSA	PSA	PSP	Electric Drives And Their Control
WED	PSE	PSE	NCES	EOPS /OS		PSA	PSA	PSP	Electric Drives And Their Control
THURS	AIT	NCES	PSA	PSP		PSM Lab			Electric Drives And Their Control
FRI	AIT	NCES	PSP	PSA		Seminar / Industrial Economics & Management			Electric Drives And Their Control
SATUR	NCES	PSE	PSE	EOPS /OS		Project			Electric Drives And Their Control

Subject

NCES

PSP

PSA

PSE

AIT

OS

EOPS

Power System Modelling Lab

Power system Planning Lab

Faculty

Ashika Sharma

CPJan

CPJain

Rajkumar Soni

Poornima Dave

Gaurav Kumawat

Rajkumar Soni

Rajkumar Soni

CPJain

Industrial Economics & Management Lab CPJain

Signature

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2019

Time Table – II Year (IV Sem)

DAY	08:00 - 09:00	09:00- 10:00	10:00- 11:00	11:00- 12:00	12:00- 12:30	12:30 to 1:30	01:30- 2:30	02:30- 03:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	EM-II	SS	PE	PE		EMI	EMI	BIO	Electrical Circuit Analysis
TUES	EM-II	PE	SS	EMI		BIO	SS	MEFA	Electrical Circuit Analysis
WED	EM-II Lab		PE Lab			DE	DE	DE	Electrical Circuit Analysis
THURS	EMI	EM-II	PE	EMI		EM Lab			Electrical Circuit Analysis
FRI	EM-II	BIO	PE	DE		BIO	MEFA	MEFA	Electrical Circuit Analysis
SATUR	SS	EM-II	SS	DE		DE Lab			Electrical Circuit Analysis

Subject

EM-II

SS

PE

EMI

BIO

DE

MEFA

DE Lab

EM Lab

Faculty

Lalita

Vivek Jain

Abrar Ahmed

Ashika Sharma

Yasmin Ali

Yogendra Solanki

Mr. Shambhu P. Choubisa

Prof. Pradeep C.

Ashika

EM-II Lab

PE Lab

Lalita

Rajkumar

15/05/2019

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2019

DAY	08:00 - 09:00	09:00- 10:00	10:00- 11:00	11:00- 12:00	12:00- 12:30	12:30 to 1:30	01:30- 2:30	02:30- 03:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	EM-II	SS	PE	PE		EMI	EMI	BIO	Electrical Circuit Analysis
TUES	EM-II	PE	SS	EMI		BIO	SS	MEFA	Electrical Circuit Analysis
WED	EM-II Lab		PE Lab			DE	DE	DE	Electrical Circuit Analysis
THURS	EMI	EM-II	PE	EMI		EM Lab			Electrical Circuit Analysis
FRI	EM-II	BIO	PE	DE		BIO	MEFA	MEFA	Electrical Circuit Analysis
SATUR	SS	EM-II	SS	DE		DE Lab			Electrical Circuit Analysis

Subject

Faculty

EM-II

Lalita

PE Lab

Rajkumar

SS

Vivek Jain

PE

Abrar Ahmed

EMI

Ashika Sharma

BIO

Yasmin Ali

DE

Yogendra Solanki

MEFA

Paras kothari

DE Lab

Prof. Pradeep C.

EM Lab

Ashika

EM-II Lab

Lalita

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2019

Time Table – IV Year (VIII Sem)

DAY	10:00-11:00	11:00-12:00	12:00-1:00	1:00-2:00	2:00-2:30	2:30-05:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 2:30 to 5:00
MON	PPS	PPS	EDTC	EDTC		EDTC lab	Electric Drives And Their Control
TUES	UEP	PPS	EDTC	EHV		CBPS Lab	Electric Drives And Their Control
WED	UEP	PPS	EHV	EHV		HV Lab	Electric Drives And Their Control
THURS	UEP	EHV	EDTC	EDTC		Seminar	Electric Drives And Their Control
FRI	UEP	EHV	EDTC			Project /Activity	Electric Drives And Their Control
SATUR	PPS	PPS	EHV	UEP		Project /Activity	Electric Drives And Their Control

Subject

EDTC

EHV AC/ DC

PPS

UEP

EDTC Lab

CBPS Lab

HV Lab

Faculty

Abrar Ahmed

CPJain

Raj kumar

CPJain

Abrar Ahmed

Rajkumar

CPJain

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

2019/03/20

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2019

Time Table – II Year (III Sem)

DAY	08:00 - 09:00	09:00- 10:00	10:00- 11:00	11:00- 12:00	12:00- 12:30	12:30- 1:30	1:30 to 2:30	2:30- 3:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	EM-I	AEM-I	EM-I	TC		AE	EMFT	PGP	Electrical Circuit Analysis
TUES	EM-I	AE	ECA	ECA		TC	EMFT	EMFT	Electrical Circuit Analysis
WED	ECD Lab		ECA	ECA		PGP	PGP	Lib	Electrical Circuit Analysis
THURS	EMFT	AEM-I	EMFT	ECA		Analog Electronics Lab (NK)			Electrical Circuit Analysis
FRI	EMFT	AEM-I	AE	AE		EM Lab			Electrical Circuit Analysis
SATUR	EM-I	EM-I	EMFT	AE		PGP	PGP	Lib	Electrical Circuit Analysis

Subject

EM-I

EMFT

PGP

ECA

AE

AEM

TC

AE Lab

Electrical Circuit Design Lab

EM- I Lab

Faculty

Lalita

Payal Paliwal

CPJain

Abrar Ahmed

Nitin kothari

Dr. Payal Jain

Hitkaran Singh Ranawat

Nitin kothari

Rajkumar

Lalita

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY
2019-2020

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2019

Time Table – III Year (V Sem)

DAY	8:00 to 9:00	9:00 to 10:00	10:00-11:00	11:00-12:00	12:00 - 12:30	12:30 to 1:45	1:45 to 3:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	MP	EMD/NN	PS-I	CS(CPJ)	Lunch	PS-I	EM/VLSI	Electric Drives
TUES	MP	EMD/NN	CS(CPJ)	PS-I		EW/AC	EM/VLSI	Electric Drives
WED	MP	EMD/NN	CS(CPJ)	EM/VLSI		System Prog. Lab		Electric Drives
THURS	MP	EMD/NN	PS-I	CS(CPJ)		PS- I Lab (RKS)		Electric Drives
FRI	MP	EMD/NN	EM/VLSI	EW/AC		Control System Lab		Electric Drives
SATUR	PS-I	EW/AC	CS(CPJ)	EW/AC		MP Lab (YSS)		Electric Drives

Subject

MP

EMD

PS-I

CS

EM

VLSI

NN

EW

AC

Control System Lab

MP Lab

System Prog Lab

PS-I Lab

Faculty

Yogendra Solanki

Lalita

Rajkumar

CPJain

Payal Paliwal

Yogendra Solanki

Dr. Jitendra Srimali

Payal Plaiwal

Payal Paliwal

CPJain

Yogendra Solanki

CPJain

Rajkumar Soni

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2019

Time Table – IV Year (VII Sem)

DAY	8:00 to 9:00	9:00 to 10:00	10:00-11:00	11:00-12:00	12:00-12:30	12:30- to 01:20	01:20- 02:10	02:10- 03:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	AIT	PSE	EOPS /OS	EOPS /OS	Lunch	Power system Planning			Electric Drives And Their Control
TUES	AIT	NCES	PSE	EOPS /OS		PSA	PSA	PSP	Electric Drives And Their Control
WED	PSE	PSE	NCES	EOPS /OS		PSA	PSA	PSP	Electric Drives And Their Control
THURS	AIT	NCES	PSA	PSP		PSM Lab			Electric Drives And Their Control
FRI	AIT	NCES	PSP	PSA		Seminar / Industrial Economics & Management			Electric Drives And Their Control
SATUR	NCES	PSE	PSE	EOPS /OS		Project			Electric Drives And Their Control

Subject

NCES

PSP

PSA

PSE

AIT

OS

EOPS

Power System Modelling Lab

Power system Planning Lab

Industrial Economics & Management Lab

Faculty

CPJain

CPJain

CPJain

Rajkumar Soni

Kirti

Gaurav Kumawat

Rajkumar Soni

Rajkumar Soni

CPJain

CPJain

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2020

Time Table – II Year (IV Sem)

DAY	08:00 - 09:00	09:00- 10:00	10:00- 11:00	11:00- 12:00	12:00- 12:30	12:30 to 1:30	01:30- 2:30	02:30- 03:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 12:30 to 3:00
MON	EM-II	SS	PE	PE		EMI	EMI	BIO	Electrical Circuit Analysis
TUES	EM-II	PE	SS	EMI		BIO	SS	MEFA	Electrical Circuit Analysis
WED	EM-II Lab		PE Lab			DE	DE	DE	Electrical Circuit Analysis
THURS	EMI	EM-II	PE	EMI		EM Lab			Electrical Circuit Analysis
FRI	EM-II	BIO	PE	DE		BIO	MEFA	MEFA	Electrical Circuit Analysis
SATUR	SS	EM-II	SS	DE		DE Lab			Electrical Circuit Analysis

Subject

EM-II

SS

PE

EMI

BIO

DE

MEFA

DE Lab

EM Lab

EM-II Lab

PE Lab

Faculty

Lalita

Vivek Jain

Abrar Ahmed

Ashika Sharma

Yasmin Ali

Yogendra Solanki

Paras kothari

Prof. Pradeep C.

Ashika

Lalita

Rajkumar

Prof. Pradeep C.

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2020

Time Table – III Year (VI Sem)

DAY	09:00 - 10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00 to 1:30	1:30 to 2:45	2:45 to 4:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 2:00 to 4:00
MON	PP	CA	ED	EECA/DBMS	LUNCH	PS-II	SGT/PSP	Electric Drives
TUES	PS-II	CA	ED	PS-II		PP	CA	Electric Drives
WED	ED Lab		ED	EECA/DBMS		Power System Protection Lab		Electric Drives
THURS	CA	PS-II	SGT/PSP	ED		PP	EECA/DBMS	Electric Drives
FRI	PP	PS-II	EECA/DBMS	PP		Power System - II Lab		Electric Drives
SATUR	CA	EECA/DBMS	SGT/PSP	SGT/PSP		Modelling and simulation lab		Electric Drives

Subject

CA

PP

ED

EECA

PS-II

SGT

PSP

DBMS

Modelling and simulation lab

Power System - II Lab

Power System Protection Lab

ED Lab

Faculty

Gaurav Kumawat

Rajkumar

Abrar Ahmed

CPJain

Rajkumar

Lalita

CPJain

Pankaj Chittora

Rajkumar

Rajkumar

CPJain

Pushkar lohar

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY

Techno India NJR Institute of Technology

Department of Electrical Engineering (EE & EEE)

Session: 2020

Time Table – IV Year (VIII Sem)

DAY	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-1:30	1:30-04:00	After Completion of RTU Syllabus Industry Aligned Knowledge Classes 2:00 to 4:00
MON	EDTC	EHV	PPS	PPS	L U N C H	EDTC Lab	Electric Drives And Their Control
TUES	UEP	EDTC	EHV	PPS		HV Lab	Electric Drives And Their Control
WED	EDTC	EDTC	EHV	EHV		CBPS Lab	Electric Drives And Their Control
THURS	EDTC	EDTC	EHV	EHV		Seminar	Electric Drives And Their Control
FRI	PPS	PPS	UEP	UEP		Project /Activity	Electric Drives And Their Control
SATUR	UEP	PPS	UEP	PPS		Project /Activity	Electric Drives And Their Control

Subject

EDTC

EHV AC/ DC

PPS

UEP

EDTC Lab

CBPS Lab

HV Lab

Faculty

Abrar Ahmed

CPJain

Raj kumar

CPJain

Abrar Ahmed

Rajkumar

CPJain

TECHNO INDIA NJR INSTITUTE OF TECHNOLOGY