

Passionate, dedicated and hardworking individual believes in steady employment and self and organizational growth.

# Experience

Aug, 2019 - Present

Assistant professor in Electronics Deptt. /Techno India NJR Institute of Technology, Udaipur (Raj.)

### 2016-18

**Teaching assistant in Electronics Deptt. / University college of Engineering, Kota (Raj.)** Aug, 2016 – July, 2018

### 2015-16

Lecturer in Electronics Deptt. / Dr. D.N. Mutha Institute of Diploma Engineering (BTER, Jodhpur), Barmer (Raj.)

Jan, 2015 – June, 2016

# Education

### 2016-18

**M.Tech.** (Digital Communication) / University College of Engineering (Rajasthan Technical University), Kota

• Aggregate score: - 74.68% (Honours).

### 2010-14

**B.Tech.** (Electronics and Communication Engineering) / JIET Group of Institution, Jodhpur

• Aggregate score: - 76.86% (Honours.)

### 2009-10

Senior secondary school/ Kendriya Vidhyalaya, Jalipa Cantt, Barmer (CBSE)

• Final score: - 83.60%

### 2007-08

### Secondary school/ Board of Secondary Education, Rajasthan (RBSE)

• Final score: - 86.33%

# **Publications:**

- Paliwal P., Sharma J. B. and Nath V., "Comparative study of FFA architectures using different multiplier and adder topologies", *Microsyst Technol* (2019). [Springer publication]
- Paliwal P., Sharma J. B., "Efficient FPGA Implementation architecture of Fast FIR Algorithm using Han-Carlson adder based Vedic multiplier," in proceedings of the International Conference on Inventive Research in Computing Applications (ICIRCA 2018), pp 643-646. [IEEE Xplore]

Paliwal P., Sharma J. B., "Fast FIR Algorithm based symmetric FIR filter using Han-Carlson adder and vedic multiplier", *International Journal of pure and applied mathematics*, vol. 118, no. 24, 2018. [SCOPUS Indexed Journal]

### Skills

- Working knowledge of MATLAB, XILINX Vivado, Optiwave, PCB Wizard, MS office software
- Fundamental knowledge of VHDL, C++, C, python and SQL.
- Done the certified courses on:
  - Fundamentals of Scalable Data Science by IBM
  - o Advanced Machine Learning and Signal Processing by IBM
  - Python Data Analysis by Rice University
  - o Introduction to Data Science in Python by Michigan University
  - AI For Everyone by deeplearning.ai
  - o Python for everybody (Specialization including 5 certified course) by Michigan University
  - SQL for Data Science by University of California, Davis
  - MATLAB Onramp by Mathworks corp.
  - Machine Learning Onramp by Mathworks corp.
  - Deep Learning Onramp by Mathworks corp.

### Activities

#### **Faculty development program**

• Attended one week online faculty developement program on "Recent Trends in Electronics and Communication Engineering" organized by Chennai Institute of Technology during 8-12<sup>th</sup> June, 2020.

### **Summer training**

• Attended summer training program at Nokia Solution Network Pvt. Ltd., Jaipur during May-June, 2013 and acquired proficient knowledge about "Global system of mobile communication"

### **B.Tech. Project**

• Worked on project entitled, "Wireless surface electromyogram" and "Simple Digital clock using microcontroller Atmel 89C51".

### M.Tech. Project

- Worked on the thesis entitled, "Efficient FPGA implementation architectures of the Fast FIR Algorithm".
- Attended the one week workshop on "Recent trends in Digital Image Processing & Computer Vision".
- Attended workshop program on "Machine learning workshop using R and Python".
- Attended the short term course on "Digital system and digital communication".

### Achievements

- Qualified UGC-NET 2018 exam
- Qualified GATE examination in 2014 and 2015
- Secured 2<sup>nd</sup> position in Rajasthan in M.Tech.
- Secured 3<sup>rd</sup> position in B.Tech. ECE department.
- Head Girl of Kendriya Vidhyalaya, Jalipa Cantt, Barmer, during 2009-10
- Qualified in 51<sup>st</sup> state level athletic competition, 2006-07 in 3000 m

# **Personal Details**

Father's name: Mr. Durga Prasad Paliwal Mother's name: Mrs. Seema Paliwal Date of Birth: 14<sup>th</sup> July, 1992

Hobbies: Reading, art and craft, sketching, listening to music

I hereby declare that all the particulars stated in this curriculum vitae are true to the best of my knowledge and belief.

**Udaipur** Date: 22<sup>nd</sup> June, 2020

Faleway ...

Payal Paliwal